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IN THE UNITED STATES PATENT AND TRANDMARK OFFICE

Patent Application Serial No.	09/921,518
Filing Date	August 1, 2001
Inventor	John T. Moore
Assignee	Micron Technology, Inc.
Group Art Unit	2818
Examiner	Phuc T. Dang
Attorney Docket No.	MI22-1669
Title: Method of Forming Integrated Circuitry, Method of Forming Memory Circuitry, and Method of Forming Random Access Memory Circuitry	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 C.F.R. §1.56. Copies of the cited art are included. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed before the mailing of a first Office Action following the filing of a Request for Continued Examination. Therefore, no fee is believed to be required per 37 C.F.R. § 1.97(b)(4). However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated:

10-9-02

Attorney

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